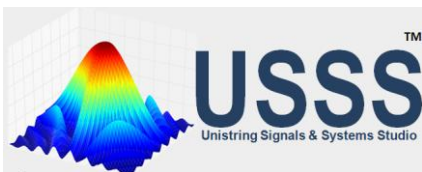


## Unistring Signals & Systems Studio (Model based design and development tool)

Developing Complex systems with high speed signal processing capabilities in short development time is a major challenge faced by today's designers. The model based design is proven to reduce the development and validation time considerably.

USSS is powerful frontend tool for algorithm design and development, with drag and drop model based design approach. The tool allows user to develop application at several levels of abstraction right from specification to RTL (register transfer level) design styles. The tool provides user friendly environment to work on various hardware platforms and allows designers to develop and test complex systems in short design cycles. The increased debugging capabilities of current release of tool allows integrated bus viewer in time and frequency domain graphs.

### Ordering Information



UTS-SW-USSS-V2

Related IP libraries: Communication system, RADAR, DSP, Control System, Computing, Control System (optional)

### Supported Target Hardware

Xilinx FPGAs: Zed board, Virtex ML605  
Analog devices : FM-COMMS 1/2/4  
UTS hardware - SDR module - 3U VPX

(contact us for additional Hardware support).

### Supported Tools

Compatible with

Simulation: Modelsim, Xilinx ISIM  
Synthesis : Xilinx ISE 12.3 or above  
Debugging: Xilinx Chipscope.  
Compilers : MATLAB, GCC compilers

### Platform support

Operating system: Windows-7 (or above)

### Key features

- Model based graphical design – Drag and drop RTL and IP components.
- Supports Xilinx FPGA hardware for design and test.
- Generates the HDL code for RTL block diagram and models.
- User selectable signed, unsigned arithmetic and fixed point precisions for FPGA algorithm development.
- Bus viewer for time and frequency domain plots
- Automatic debugging cores insertion for on chip test and validation
- Generates test benches and scripts for fast simulation and validation.
- Supports libraries for Communication system, RADAR, DSP, Control System, Computing, Control System, (optional features)
- Connects over Xilinx IMPACT tools for FPGA configuration.
- Drivers for working on Zynq boards.

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